

Fig. 10

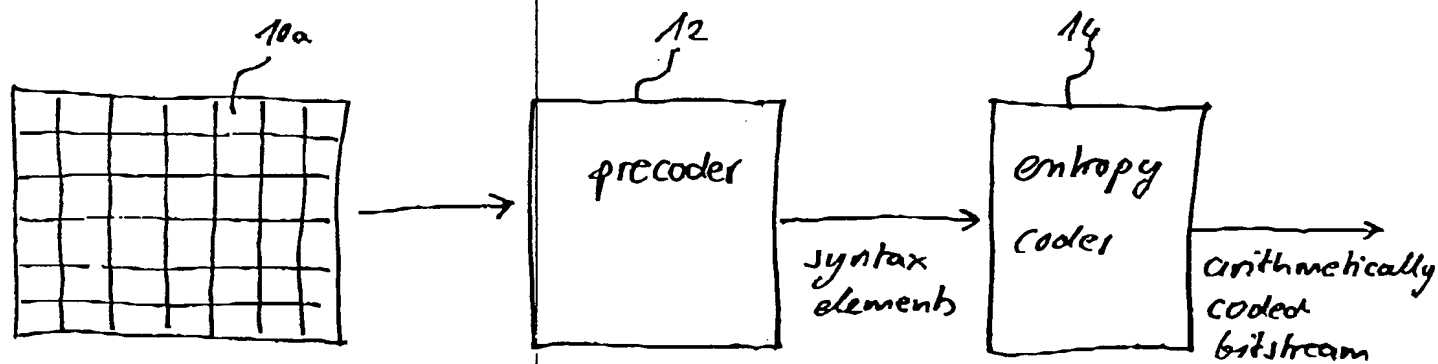


Fig. 1

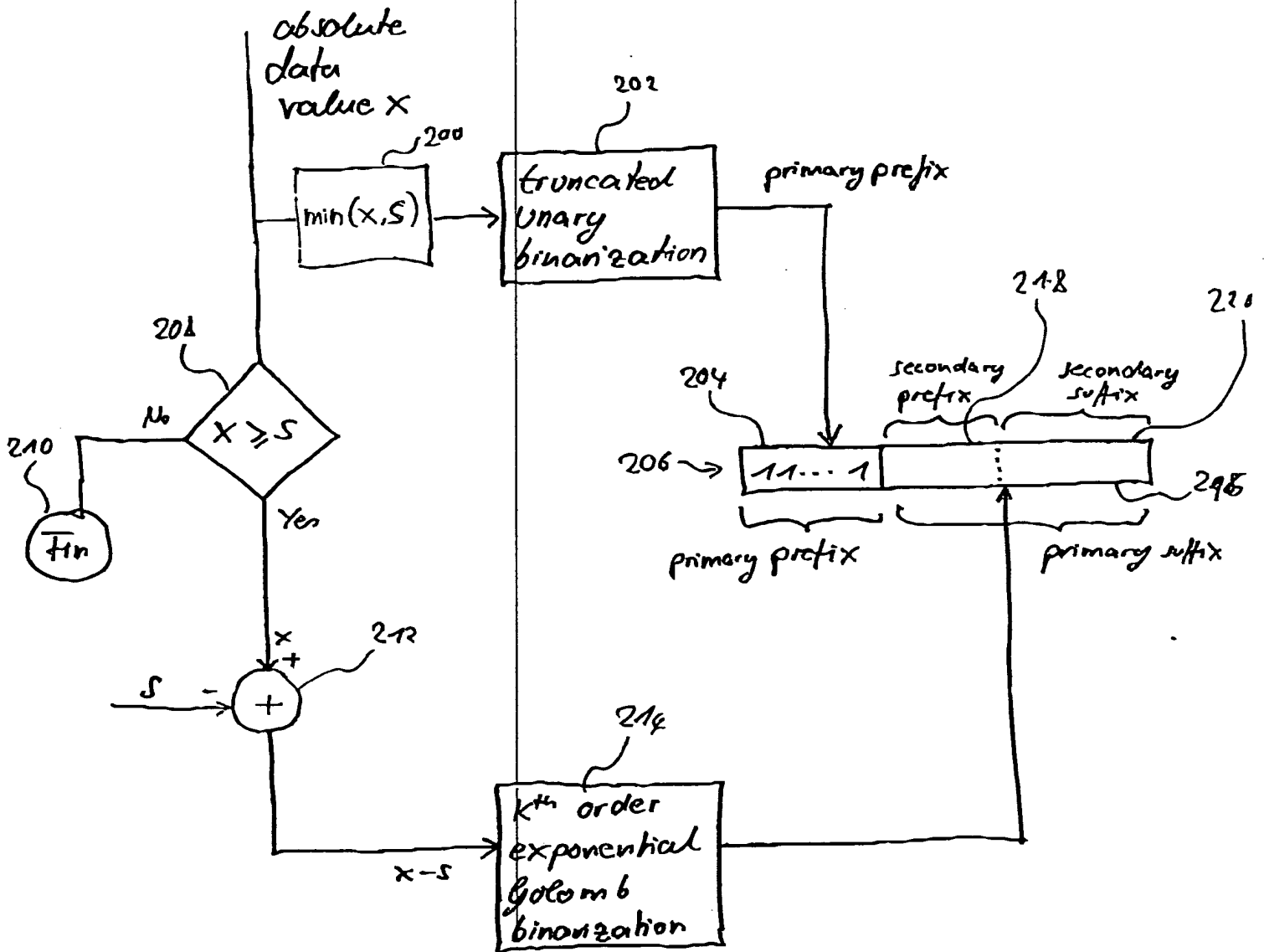


Fig. 4

Q	A	B	C	D	E	F	G	H
I	a	b	c	d				
J	e	f	g	h				
K	i	j	k	l				
L	m	n	o	p				

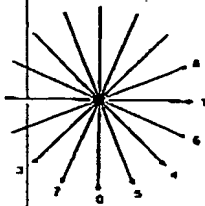


Fig. 15

252

250

Abs. value	Bin string														EG0 suffix
	TU prefix														
1	0														
2	1 0														
3	1 1 0														
4	1 1 1 0														
5	1 1 1 1 0														
...															
...															
13	1 1 1 1 1 1 1 1 1 1	1	1	0											
14	1 1 1 1 1 1 1 1 1 1	1	1	1	0										
15	1 1 1 1 1 1 1 1 1 1	1	1	1	1	0									
16	1 1 1 1 1 1 1 1 1 1	1	1	1	1	1	0								
17	1 1 1 1 1 1 1 1 1 1	1	1	1	1	1	1	0							
18	1 1 1 1 1 1 1 1 1 1	1	1	1	1	1	1	1	0						
19	1 1 1 1 1 1 1 1 1 1	1	1	1	1	1	1	1	1	0					
20	1 1 1 1 1 1 1 1 1 1	1	1	1	1	1	1	1	1	1	0				
...															
bin	1 2 3 4 5 6 7 8 9 10 11 12 13 14	15	16	17	18	19	...								

254

256

Fig. 5

Abs. value	Bin string	
	TU prefix	EG3 suffix
0	0 ← 270	
1	1 0	
2	1 1 0	
⋮		
7	1 1 1 1 1 1 1 0	
8	1 1 1 1 1 1 1 1 0	
9	1 1 1 1 1 1 1 1 1	0 0 0 0
10	1 1 1 1 1 1 1 1 1	0 0 0 1
11	1 1 1 1 1 1 1 1 1	0 0 1 0
⋮		⋮
16	1 1 1 1 1 1 1 1 1	0 1 1 1
17	1 1 1 1 1 1 1 1 1	1 0 0 0 0 0

250

252

254

256

258

Fig 6

```
300  k = 3
302  if( Abs( mvd ) >= 9 ) {
304      sufS = Abs( mvd ) - 9
306      stopLoop = 0
308      do {
312          if( sufS >= ( 1 << k ) ) {
312a             put( 1 )
312b             sufS = sufS - ( 1 << k )
312c             k++
312e          } else {
312f             put( 0 )
312g             while( k-- )
312h                 put( ( sufS >> k ) & 0x01 )
312i             stopLoop = 1
312j          }
310      } while( !stopLoop )
314  } if( mvd > 0 )
      put( 0 )
      else
      put( 1 )
```

Fig.7

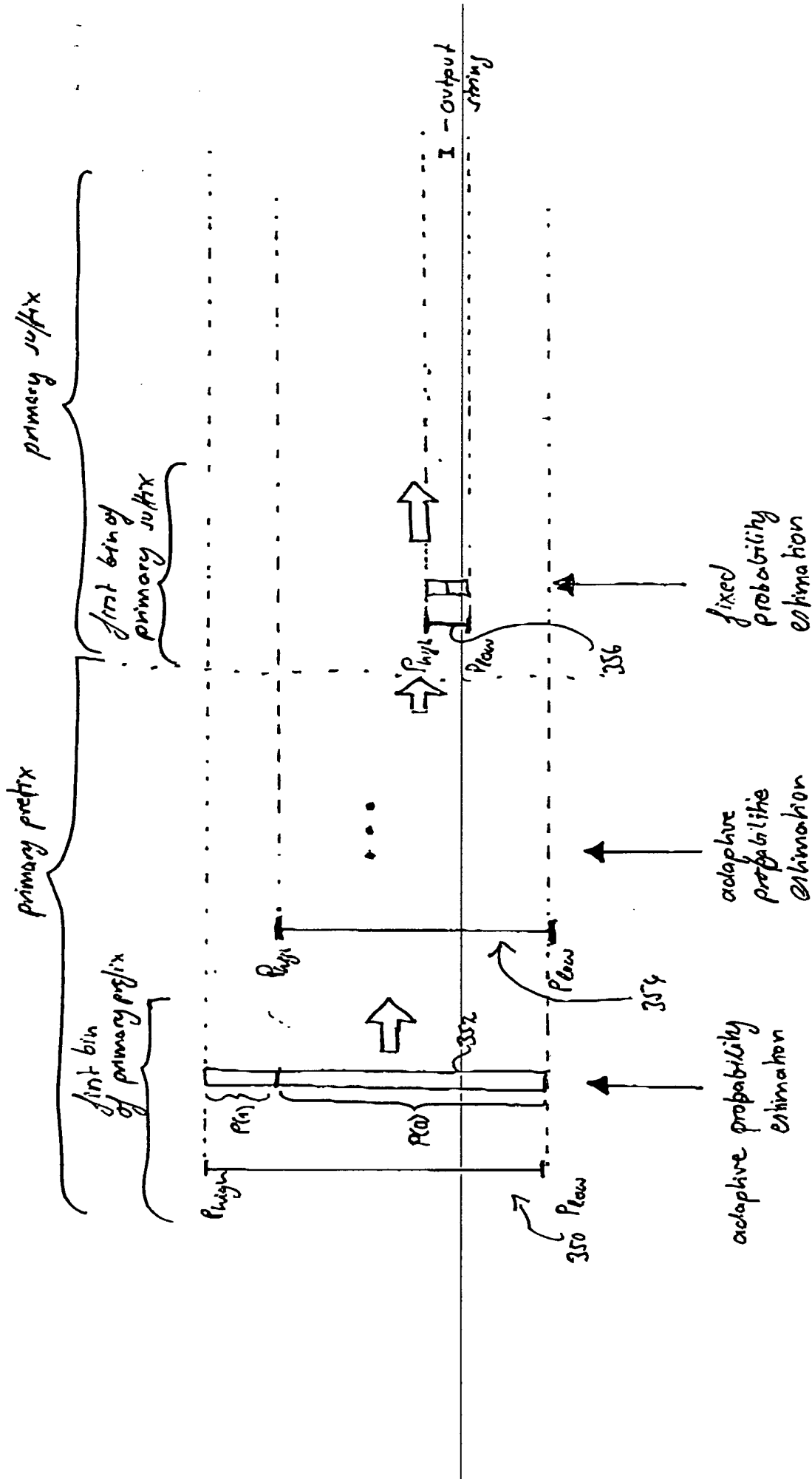


Fig. 8

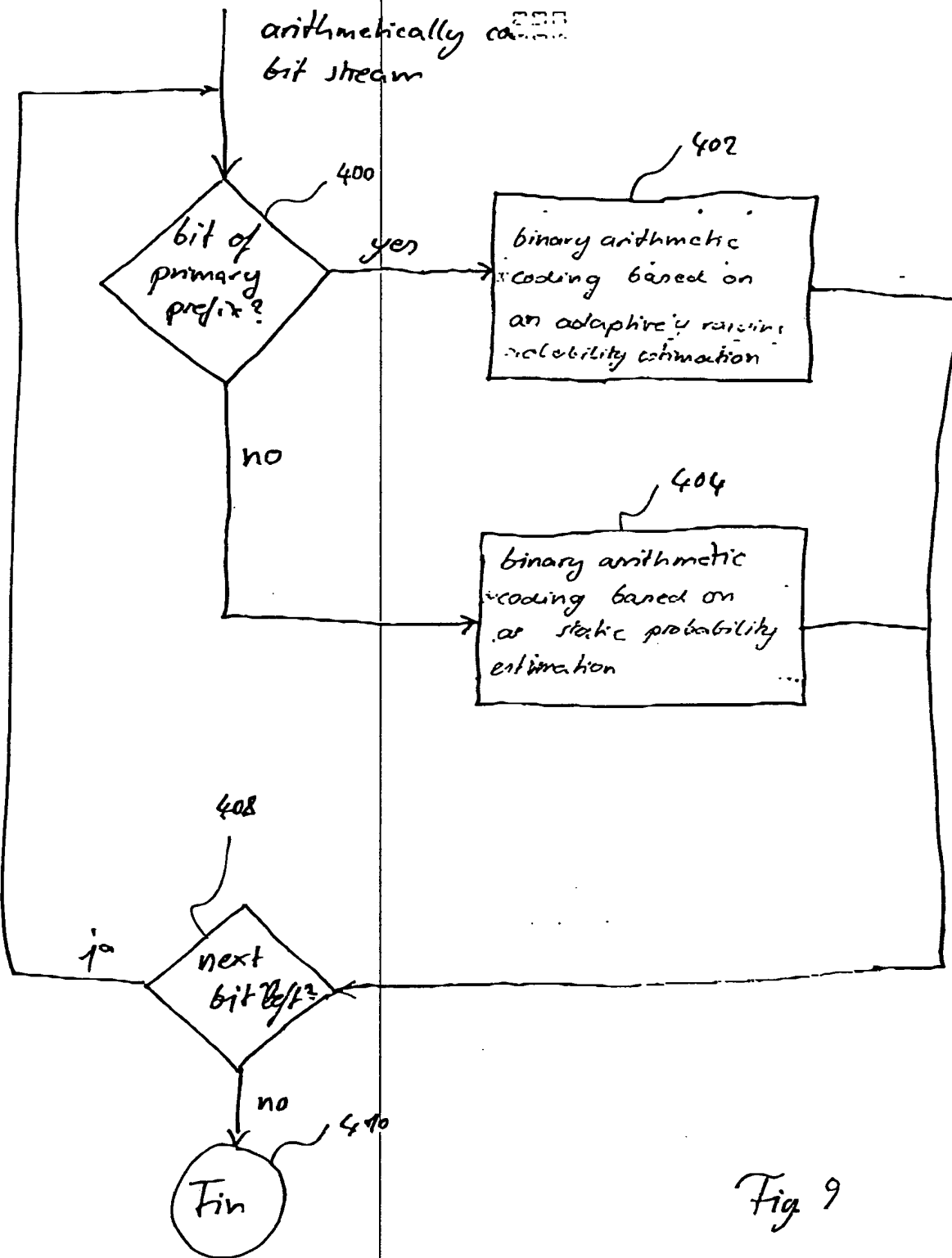


Fig 9

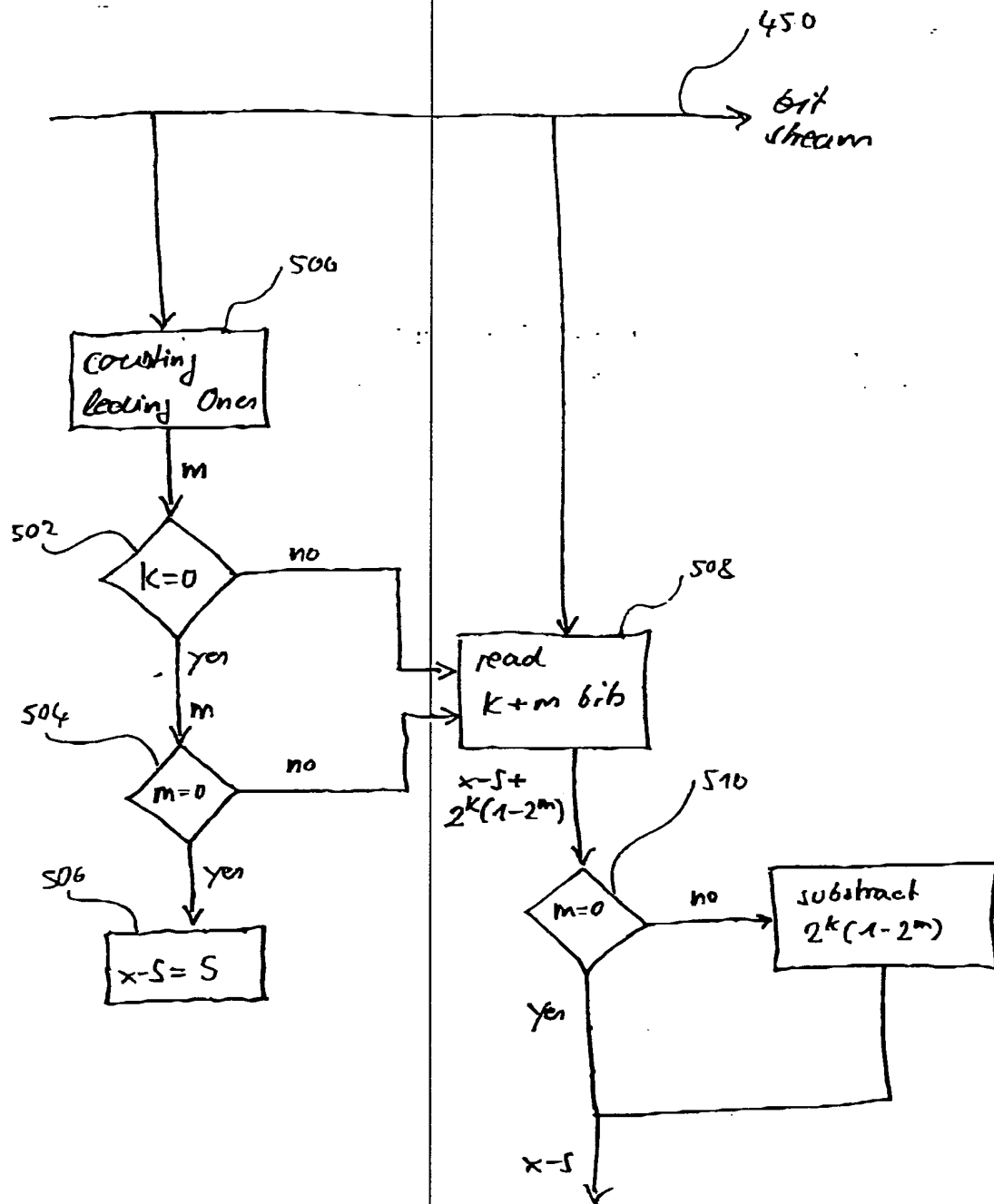


Fig. 11

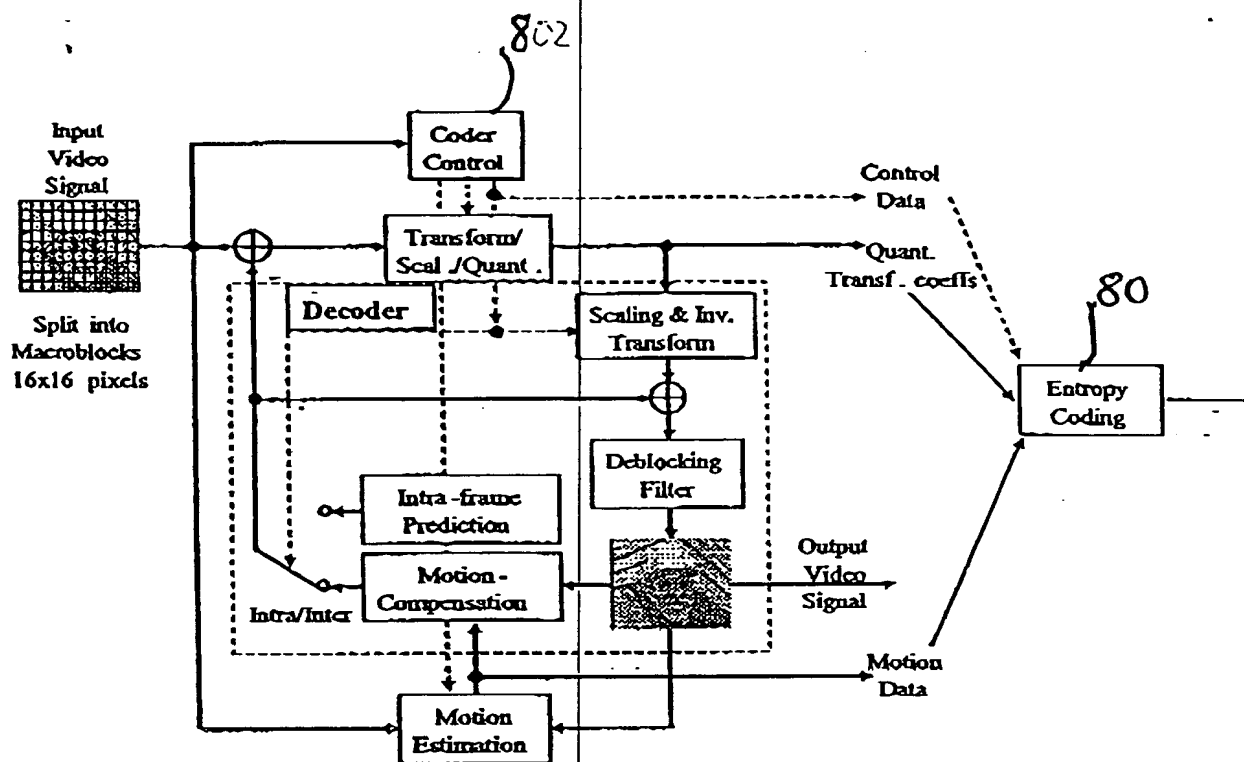


Fig. 12

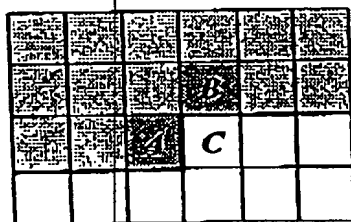


Fig. 13

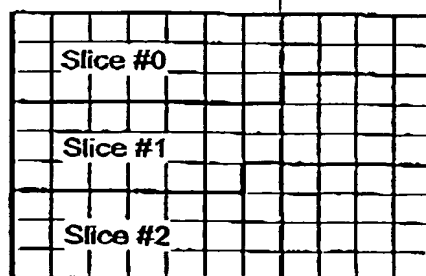


Fig. 14

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